



## MICROELECTRONICS INTEGRITY MEETING AGENDA

Meeting Date & Time: 26 July 2016 @ 0800-2000

Meeting Location: Westin Hotel – Indianapolis, Indiana

Rev 17

- 0700 Registration/Coffee
- 0800 Introduction – Captain Jeffrey T. Elder, Commanding Officer, NSWC Crane; Mr. Benjamin Harkness, Director – Global Deterrence and Defense Department, NSWC Crane; and Mr. Brian Stuffle, Manager, Flight Systems Division, NSWC Crane
- 0815 Keynote Speaker: Dr. Brett Seidle, Deputy Technical Director, NSWC Crane
- 0830 Keynote Speaker: Mr. Richard M. (Dickie) George, Senior Advisor for Cyber Security at the Johns Hopkins University Applied Physics Lab
- 0915 Keynote Speaker: Mr. John Garstka, Deputy Director for Space and Cyber, Office of the DASD for Command, Control, Communications, Cyber and Business Systems DASD (C3CB)
- 0945 Break
- 1000 Panel 1: **Deterrence and Defense Systems Commonality** (Chair - Dr. Sarah Armstrong)  
DoD continues to operate in ever increasingly constrained fiscal environments. Several strategic programs are being life extended, refreshed, and/or developed in the coming decades and the push to identify cost avoidance measures is significant. Many programs are looking to leverage skills, infrastructure, and data from an inter- and intra-service perspective. Panelists from programs with strategic requirements discuss how their organizations are approaching these efforts under a variety of acquisition strategies.  
Speakers & Topics:
- Lt Col Joseph Hank, Flight Systems Branch Lead, Ground Based Strategic Deterrent
  - Mr. Dale Crothers, Branch Head, Technical Plans, Strategic Systems Programs
  - Lt Col (S) Greg Van Dyk, Director For Engineering (Acting), Stockpile Assessments, and Responsiveness, National Nuclear Security Administration
- 1115 Guest Speaker: Mr. Jerry Roddy, Hardware Analysis Subject Matter Expert, Federal Data Systems Technical Counter Intelligence Center
- 1200 Break for Lunch
- 1315 Panel 2: **Counterfeit and Trusted Electronic Parts Policy** (Chair – Mr. Fred Schipp)  
DoD anti-counterfeit policy continues to evolve through acquisition regulation amendments. Three FAR/DFARS amendments have already been implemented, with at least four more in process. Panelists from DoD and DoD contractors discuss how their organization is implementing these new rules, and what challenges still remain in the quest to eliminate counterfeit parts and materials from defense hardware.  
Speakers & Topics:
- Mr. Wayne Moss, Material Fraud and Counterfeit Material Manager, NAVSEA 06P
  - Mr. Rick Roelecke, Director of Quality, L-3 Communications
  - Mr. Bob Bodemuller, Principle Engineer Supply Chain Quality, Lockheed-Martin Missiles and Fire Control
- 1430 Break
- 1445 Panel 3: **Cyber Hardware** (Chair – Dr. Rob Templeman)





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As we make greater efforts to secure operating systems and applications, other regions of the computation stack become attractive targets to cyber actors. The plurality of publicized hardware attack vectors, including onerous recent demonstrations, show us that this subset of cyber is critically important. Panelists from government, industry, and academia discuss general hardware challenges and the impact to cybersecurity.

- Mr. Jerry Roddy, Hardware Analysis Subject Matter Expert, Federal Data Systems Technical Counter Intelligence Center
- Dr. Matthew Hicks, Lecturer Electrical Engineering and Computer Science, University of Michigan
- Dr. Bret Michael, Professor of Computer Science and Electrical & Computer Engineering, Naval Postgraduate School
- Mr. Adam Nucci, Cyber Specialist, Office of the DASD for Command, Control, Communications, Cyber and Business Systems (Invited)

1600 Break

1615 Panel 4: **Academic Research Related To Trusted Microelectronics** (Chair – Mr. Matt Kay)  
Given the challenges associated with ensuring trust, the ability to discuss topics, present research, and foster new collaborations in the area of trusted microelectronics will be critical. This panel session hopes to facilitate these discussions and collaborations. This session is particularly focused on research efforts ongoing at universities that could be leveraged to address the growing challenges associated with ensuring trust and reliability in microelectronics.

Speakers & Topics:

- Dr. Brian King, Associate Professor of Electrical and Computer Engineering, Indiana University-Purdue University at Indianapolis
- Dr. Ernesto Marinero, Professor of Materials Engineering and Electrical and Computer Engineering, Purdue University
- Dr. Jonathan Chisum, Assistant Professor - Department of Electrical Engineering, College of Engineering, and Wireless Institute, University of Notre Dame

1730 Poster Sessions and Electronic Test System Demo's  
There will be five electronic test development organizations at this year's Microelectronics Integrity Meeting. These organizations are the current leaders in developing electronic test systems capable of detecting counterfeit electronic parts or assemblies without performing full functional test. While the organizations are not being afforded presentation time, each will have a functioning system set up adjacent to the presentation hall, to demonstrate the systems' capabilities. This is believed to be the first time all five systems have been present up at the same event. The organizations are:

- Battelle (Barricade)
- Booz Allen Hamilton (Attacus)
- Nokomis (Advanced Detection of Electronic Counterfeits)
- PFP Cybersecurity (Power Fingerprinting)
- Sandia National Lab (Power Spectral Analysis)

1830 Social – Westin Hotel

1930 Poster Winners Announced

2000 Conclude Day 1

